

L Number	Hits	Search Text	DB	Time stamp
1	156110	(system adj2 chip) or soc or csoc	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/01 08:53
2	2855	configur\$4 adj (logic or array)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/01 08:53
3	32944	dma or dmac or (direct adj memory adj access\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/01 08:53
4	42747	pla or pal or fpga or pga or fpla or fpal	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/01 08:54
5	2672	(configur\$4 adj system adj logic) or csl	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/01 08:54
6	199897	((system adj2 chip) or soc or csoc) or (configur\$4 adj (logic or array)) or (pla or pal or fpga or pga or fpla or fpal) or ((configur\$4 adj system adj logic) or csl)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/01 08:56
7	681	((system adj2 chip) or soc or csoc) or (configur\$4 adj (logic or array)) or (pla or pal or fpga or pga or fpla or fpal) or ((configur\$4 adj system adj logic) or csl) same (dma or dmac or (direct adj memory adj access\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/01 08:56
8	246	((system adj2 chip) or soc or csoc) or (configur\$4 adj (logic or array)) or (pla or pal or fpga or pga or fpla or fpal) or ((configur\$4 adj system adj logic) or csl) with (dma or dmac or (direct adj memory adj access\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/01 08:56
9	3563	(dma or dmac or (direct adj memory adj access\$3)) with (program\$4 or configur\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/01 08:57
10	44	((system adj2 chip) or soc or csoc) or (configur\$4 adj (logic or array)) or (pla or pal or fpga or pga or fpla or fpal) or ((configur\$4 adj system adj logic) or csl) with ((dma or dmac or (direct adj memory adj access\$3)) with (program\$4 or configur\$4))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/01 09:02
11	7111	((system adj2 chip) or soc or csoc) or (configur\$4 adj (logic or array)) or (pla or pal or fpga or pga or fpla or fpal) or ((configur\$4 adj system adj logic) or csl) with (ram or memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/01 09:04
12	2789	((system adj2 chip) or soc or csoc) or (configur\$4 adj (logic or array)) or (pla or pal or fpga or pga or fpla or fpal) or ((configur\$4 adj system adj logic) or csl) with bus	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/01 09:05
13	1538	326/39.ccls. or 326/41.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/01 09:05

14	37	(326/39.ccls. or 326/41.ccls.) and (dma or dmac or (direct adj memory adj access\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/10/01 09:13
15	57	(326/39.ccls. or 326/41.ccls.) and (address\$3 adj space)	USPÄT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/10/01 09:19
17	22	((326/39.ccls. or 326/41.ccls.) and (dma or dmac or (direct adj memory adj access\$3))) and ((326/39.ccls. or 326/41.ccls.) and (address\$3 adj space))	USPÄT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/10/01 09:19
16	35	((326/39.ccls. or 326/41.ccls.) and (address\$3 adj space)) not ((326/39.ccls. or 326/41.ccls.) and (dma or dmac or (direct adj memory adj access\$3)))	USPÄT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/10/01 09:20
18	12	("4500933" "4617479" "4755967" "4831285" "4876466" "4972105" "5386518" "5796944" "5796994" "5825199" "5887165" "6097988").PN.	USPAT	2002/10/01 09:23